

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

COMBINED DECLARATION FOR PATENT APPLICATION
AND POWER OF ATTORNEY

As a below inventor, I hereby declare that; My residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am an original, **JOINT** inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled: MULTIMEDIA FPGA, the specification of which:

was filed on October 5, 1998 as s/n 09/_____

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above; that the above-identified specification contains a complete and accurate description of the subject matter which is claimed and for which a patent is sought.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, CFR §1.56(a).

PRIORITY CLAIM(S):

I hereby claim benefit under Title 35, United States Code, §120 of any United States applications that are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in those prior applications in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations §1.56(a) which occurred between the filing date of the prior applications and the filing date of this application. I further claim benefit under Title 35 United States Code, §119 of any international patent applications listed below:

60/068,851 filed 12/29/97

60/068,852 filed 12/29/97

OBLIGATION TO ASSIGN: As a named inventor, I hereby affirm an obligation to assign the invention(s) set forth in this patent application to Kawasaki Steel Corporation, a Japanese corporation having a place of business at:

Hibiya Kokusai Bldg.
2-3, Uchisaiwai-cho 2-chome
Chiyoda-ku, Tokyo 100-0011, Japan

K107

FILING RECEIPT
CORRECTED



UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office
ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
09/166,499	10/05/98	2751	\$1,128.00	K107	2	22	5

GERALD E LINDEN
2716 S. CHICKASAW TR.
ORLANDO FL 32829

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Application Processing Division's Customer Correction Branch within 10 days of receipt. Please provide a copy of the Filing Receipt with the changes noted thereon.

Applicant(s)

KUMARAGURU MUTHUJUMARASWATHY, SANTA CLARA, CA;
MICHAEL D. ROSTOKER, BOULDER CREEK, CA.

CONTINUING DATA AS CLAIMED BY APPLICANT-

PROVISIONAL APPLICATION NO. 60/068,851 12/29/97
PROVISIONAL APPLICATION NO. 60/068,852 12/29/97

FOREIGN FILING LICENSE GRANTED 10/21/98

TITLE

MULTIMEDIA FPGA

PRELIMINARY CLASS: 711

DATA ENTRY BY: NIMMONS, EVELYN

TEAM: 02 DATE: 01/06/99

(see reverse)